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February 26, 2003

VIA HAND-DELIVERY

Commissioner for Patents
ATTENTION: Examiner Yennhu Huynh
Building CP4-4D19
Group Art Unit 2813
Washington, DC 20231

*Re: U.S.A. Patent Application Serial No. 09/810,595
"Memory Circuitry With Plurality of Capacitors
Received Within an Insulative Layer Well"
Our File No.: MI22-1660*

Dear Mr. Huynh:

Thank you for arranging your time to meet with me to discuss the above-identified application on Tuesday, March 4, 2003 at 1:30 p.m. Enclosed is a copy of a response to the last outstanding office action which I would like to discuss during our interview.

I expect to be out of the office from Wednesday, February 26th through Wednesday, March 5th. I look forward to meeting you on March 4th.

Very truly yours,

Mark S. Matkin

MSM/plp

Enclosures: Response to 12/03/02 Final Office Action and accompanying Version With Markings to Show Changes Made